

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): IWASAKI, et al.

Filed: November 6, 2001

For: SEMICONDUCTOR DEVICE WITH LAYERED
INTERCONNECT STRUCTURE

JC857 U.S. PTO
09/985904
11/06/01

**INFORMATION DISCLOSURE STATEMENT**
UNDER 37 CFR 1.97 AND 1.98Assistant Commissioner for Patents
Washington, D.C. 20231

November 6, 2001

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List, on a form substantially equivalent to Form PTO-1449, of documents to be considered by the Examiner during examination of the above-identified application.

All documents on the enclosed List have been submitted in connection with application Serial No. 09/255,856, filed February 23, 1999, which is being relied upon in the above-identified application under 35 USC 120. Accordingly, copies of the cited documents are not enclosed. See 37 CFR 1.98(d).

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified application. Accordingly, requirements of 37 CFR 1.97(b) are clearly satisfied.

In view of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR 1.97 and 1.98 have been satisfied, in connection with all documents on the enclosed List. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

To the extent necessary, Applicants petition for an extension of time under 37 CFR § 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 501.36931CX1) and please credit any excess fees to such Deposit Account.

Respectfully submitted,
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WIS/slk
Enclosure